

Inventor:  
Doyle  
Title:

REDDY  
SMA-001.1D  
INEXPENSIVE, RELIABLE, PLANAR RFID TAG STRUCTURE AND  
METHOD FOR MAKING SAME

SMA 001

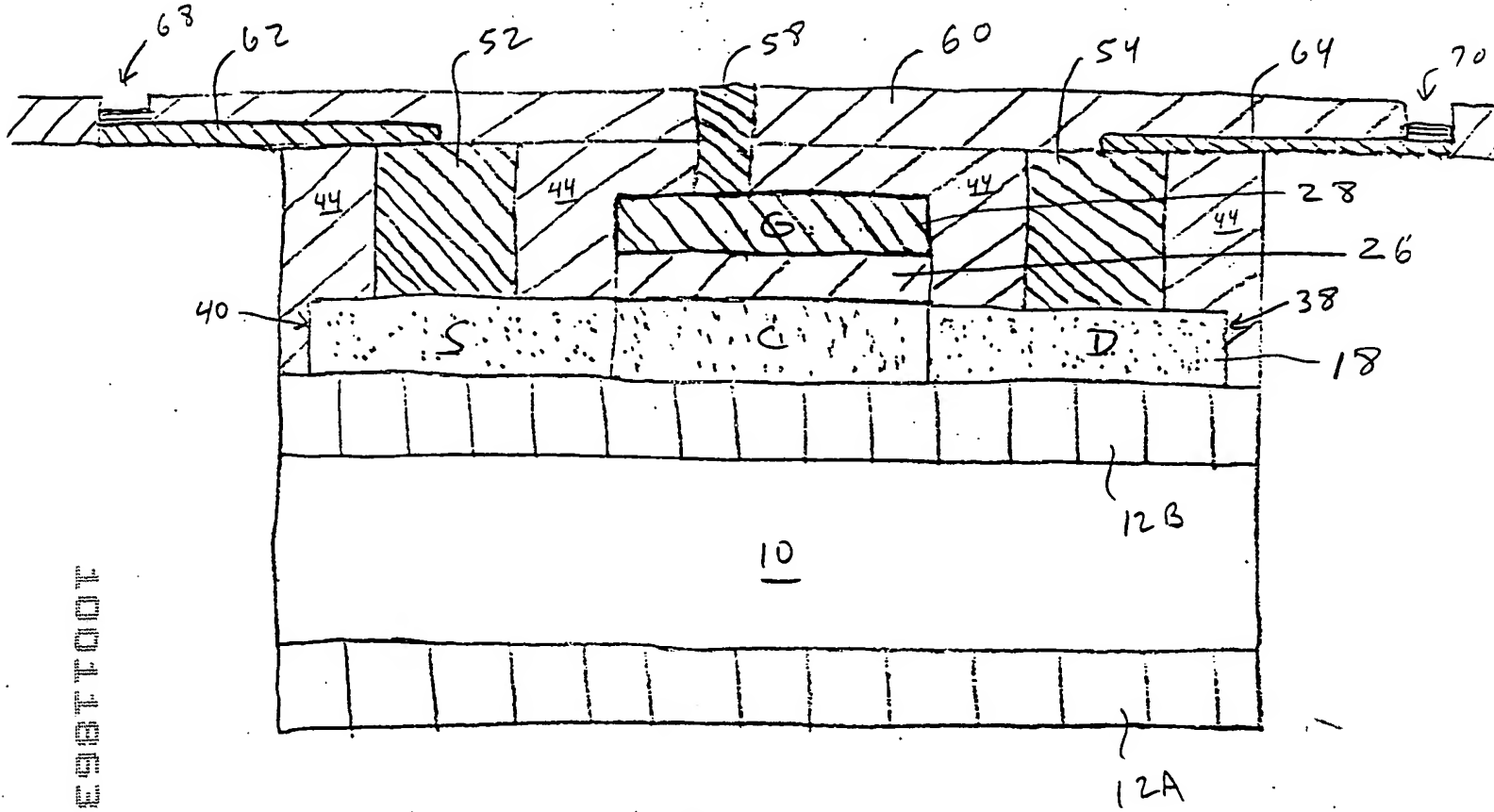


FIG. 1

100183-11201

Process Flow for Building Transistor Right Side up with Antenna on Top

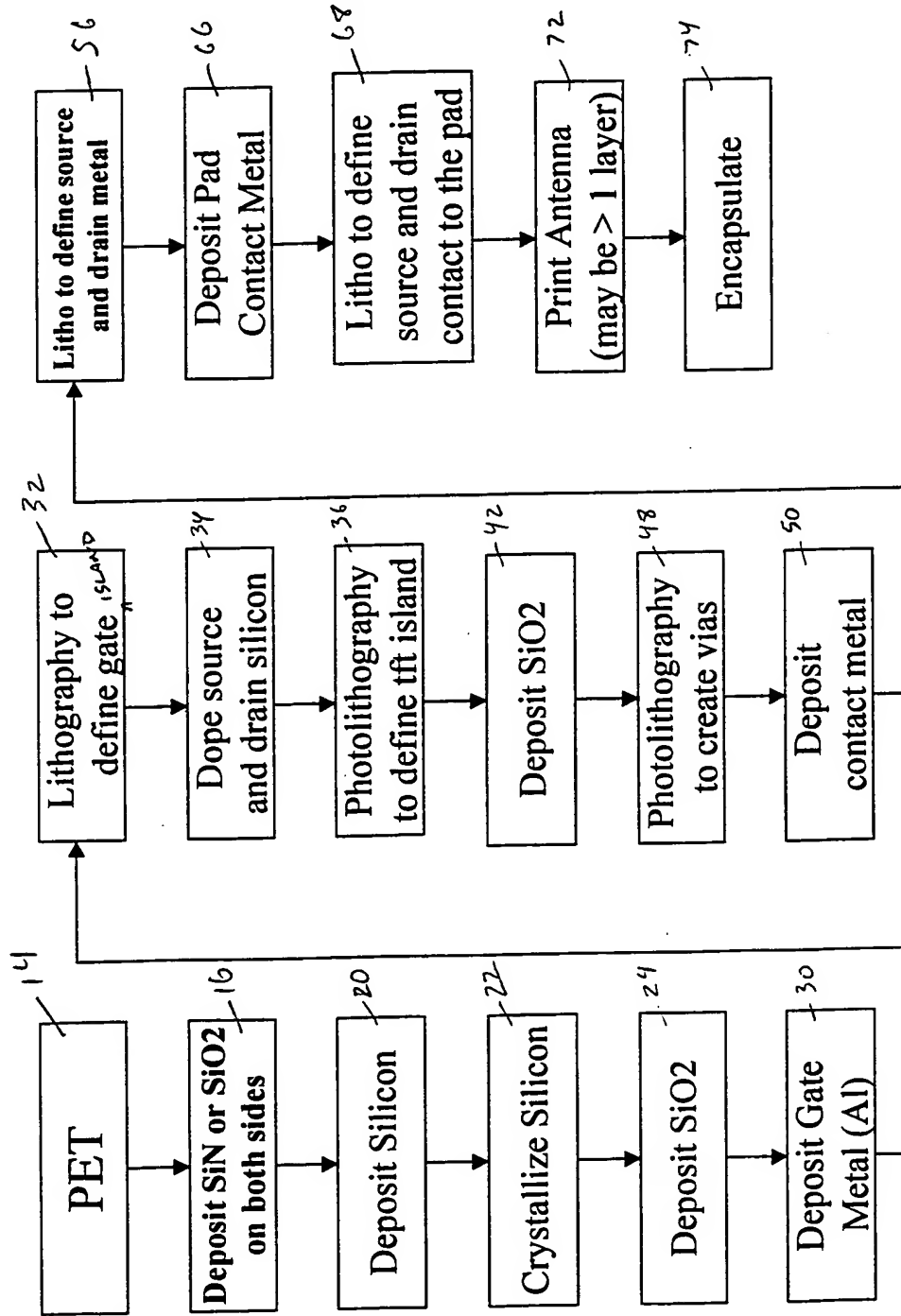
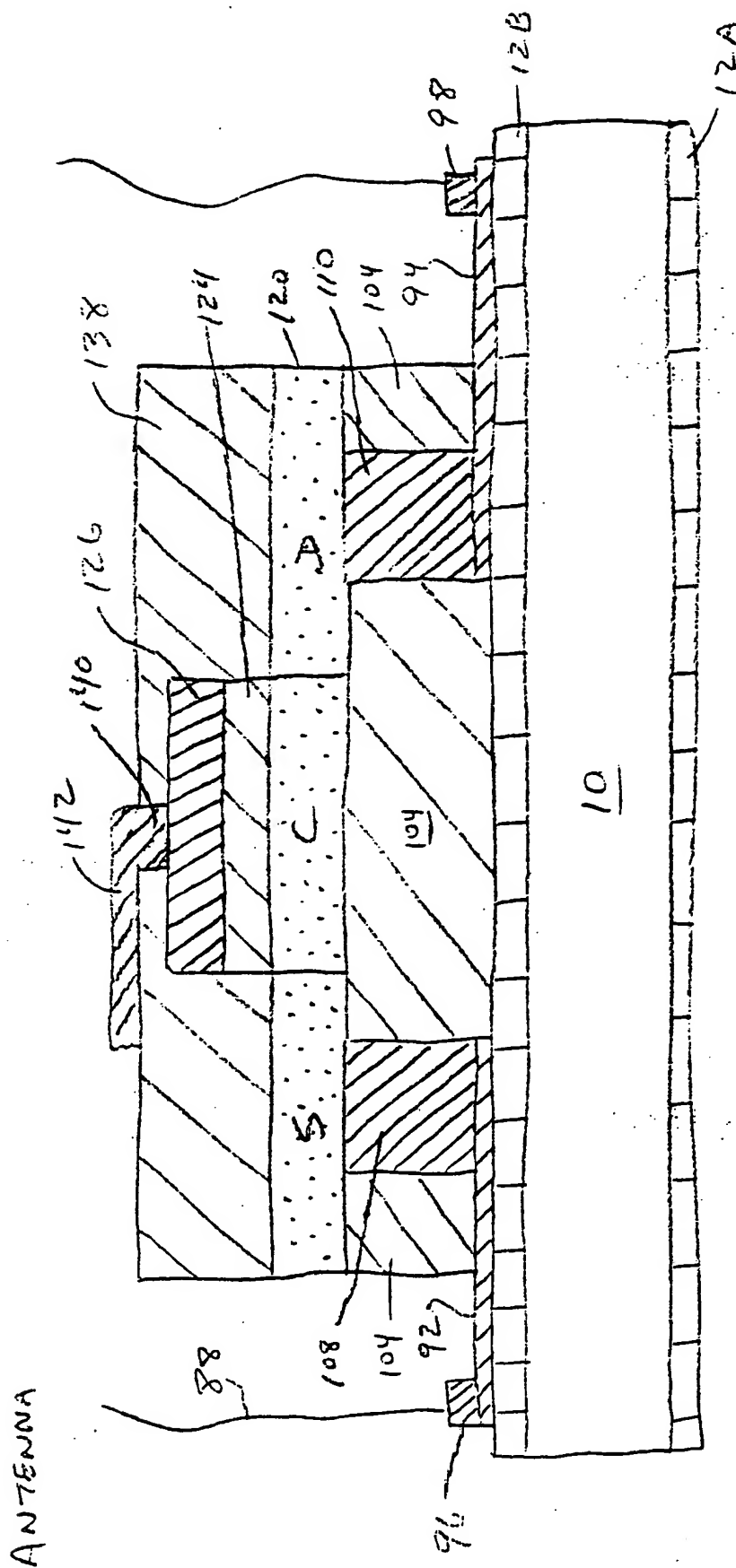


FIG. 2

Investigator: [REDACTED]  
 Document No.: [REDACTED]  
 Title: [REDACTED]

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UPSIDE DOWN TRANSISTOR ON TOP OF  
PRINTED ANTENNA

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# Process Flow for Building Transistor on Top of Antenna

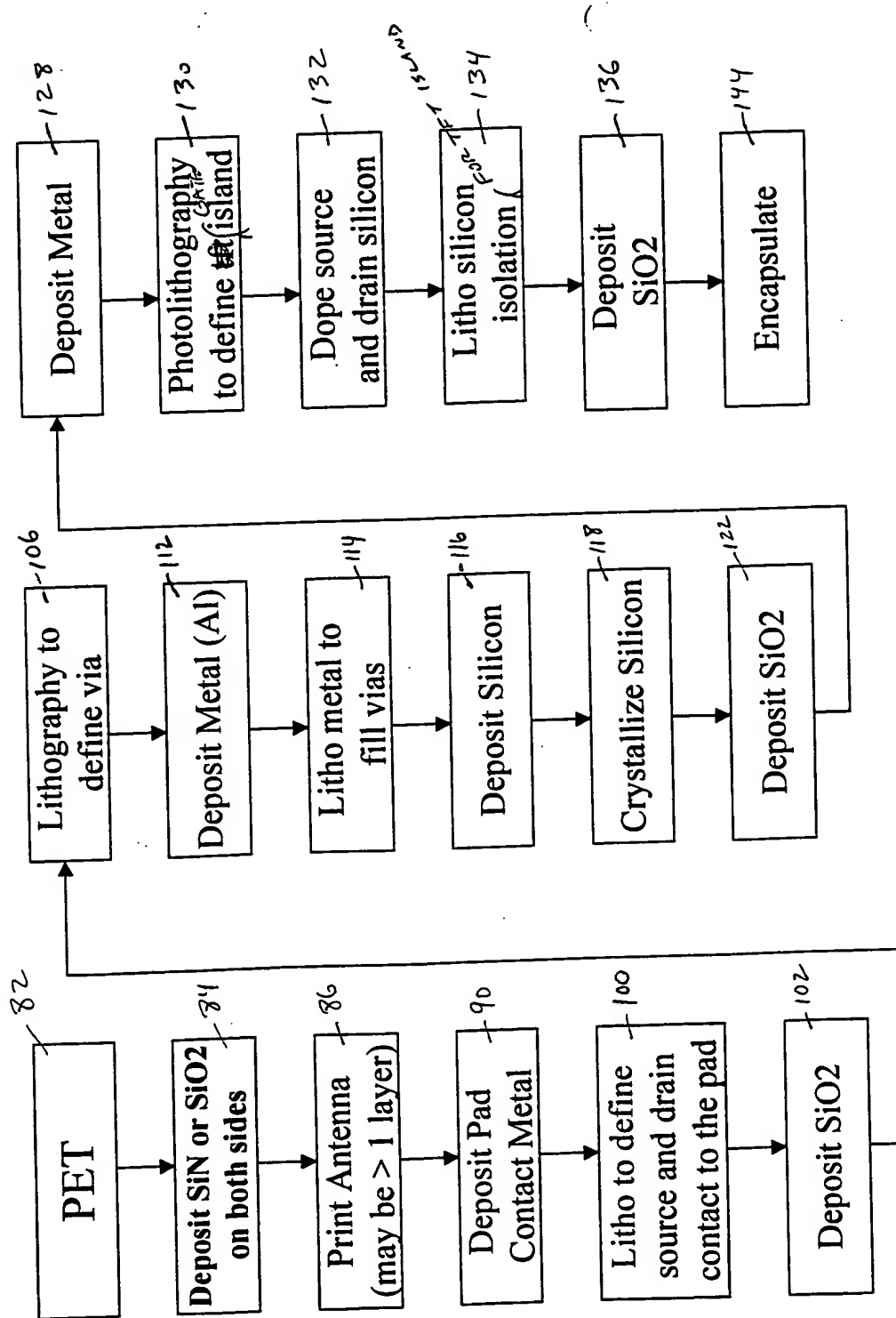
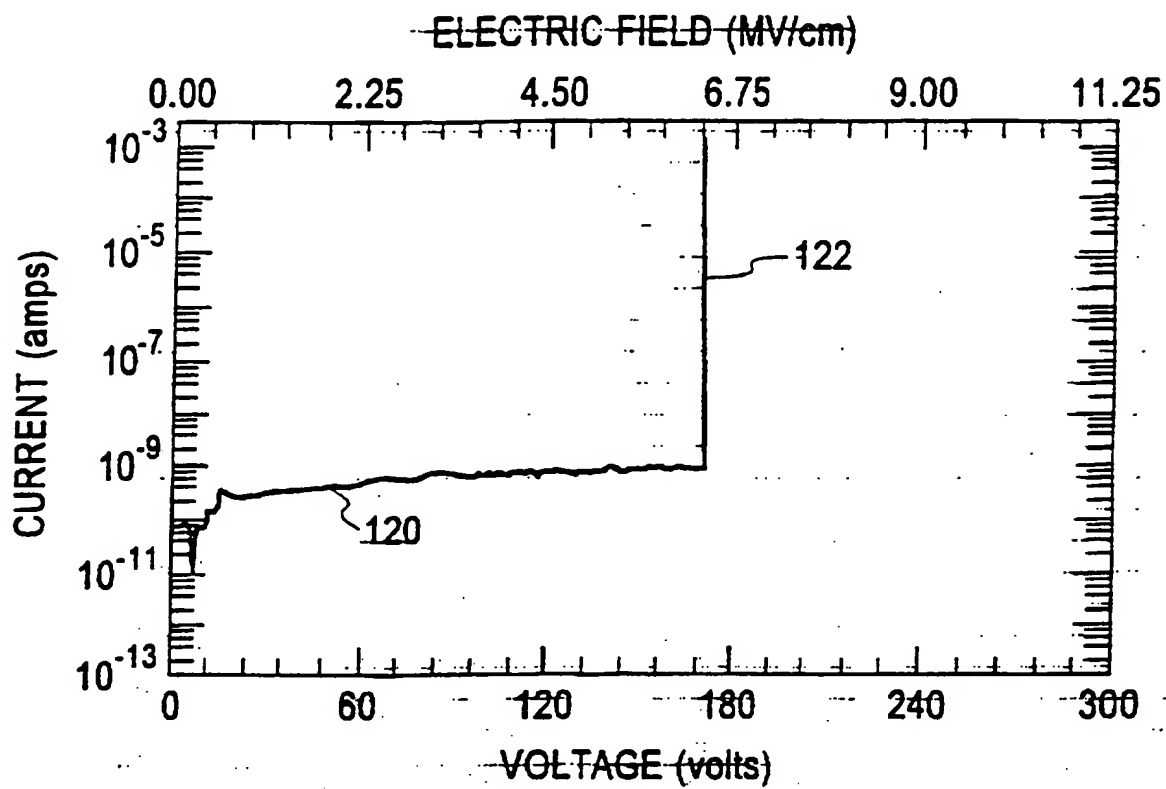


FIG. 4

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**Fig. 5**

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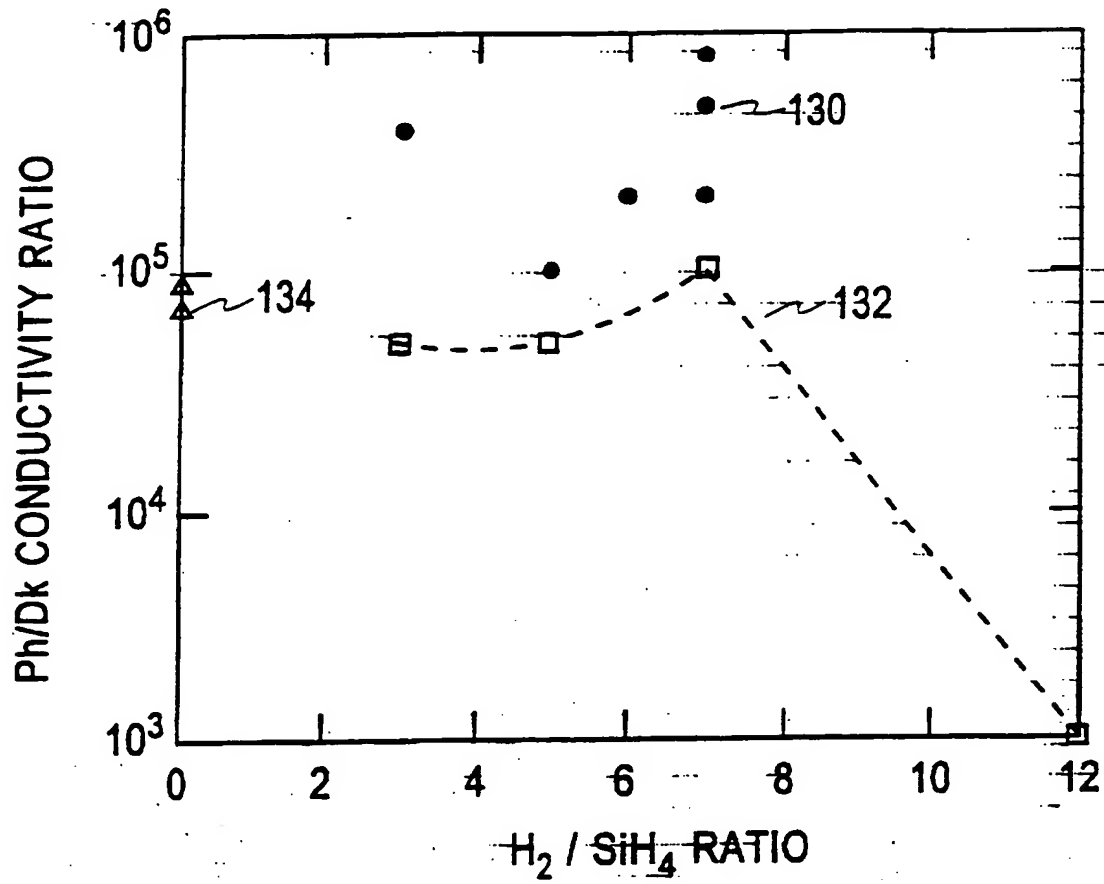


Fig. 6

## Process Flow for Building EEPROM with Antenna on Top

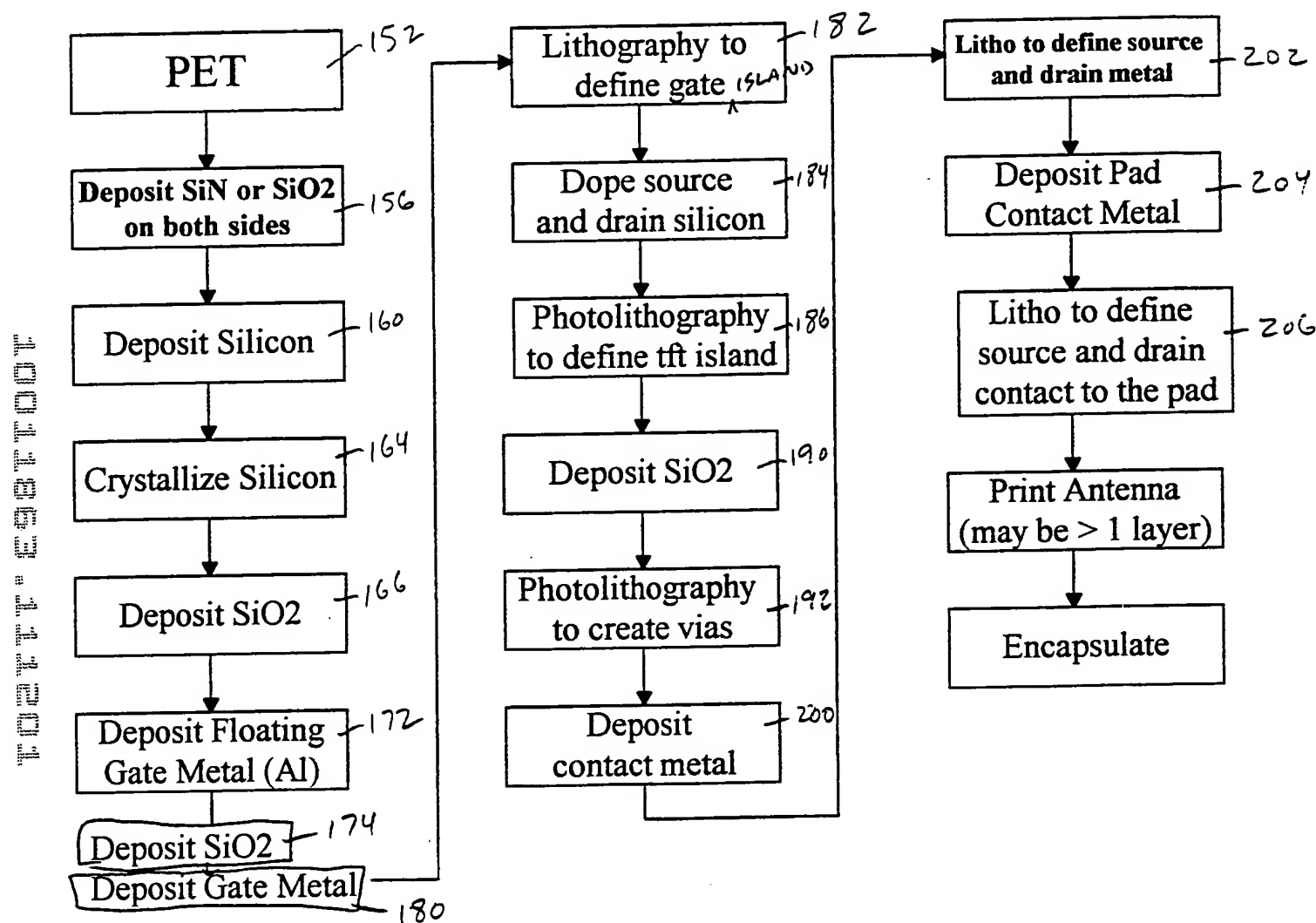
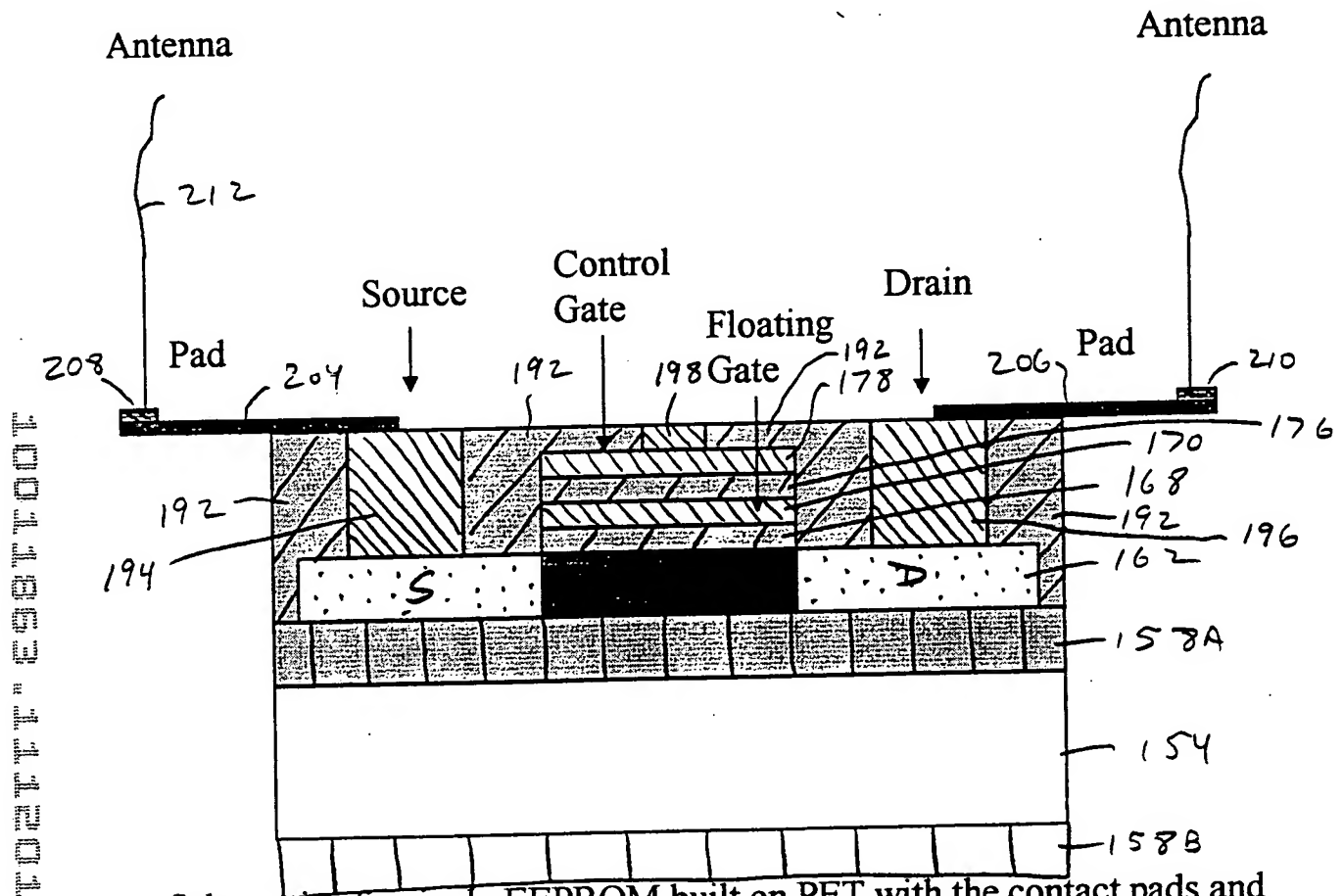


FIG. 7

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Do No.:  
Title:

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Schematic of a single EEPROM built on PET with the contact pads and the antenna printed on top of the transistor; gate will be connected to the transistors (in actual devices multiple transistors and EEPROM will be connected to the contact pads)

FIG. 8



## Process Flow for Building EEPROM on Top of Antenna

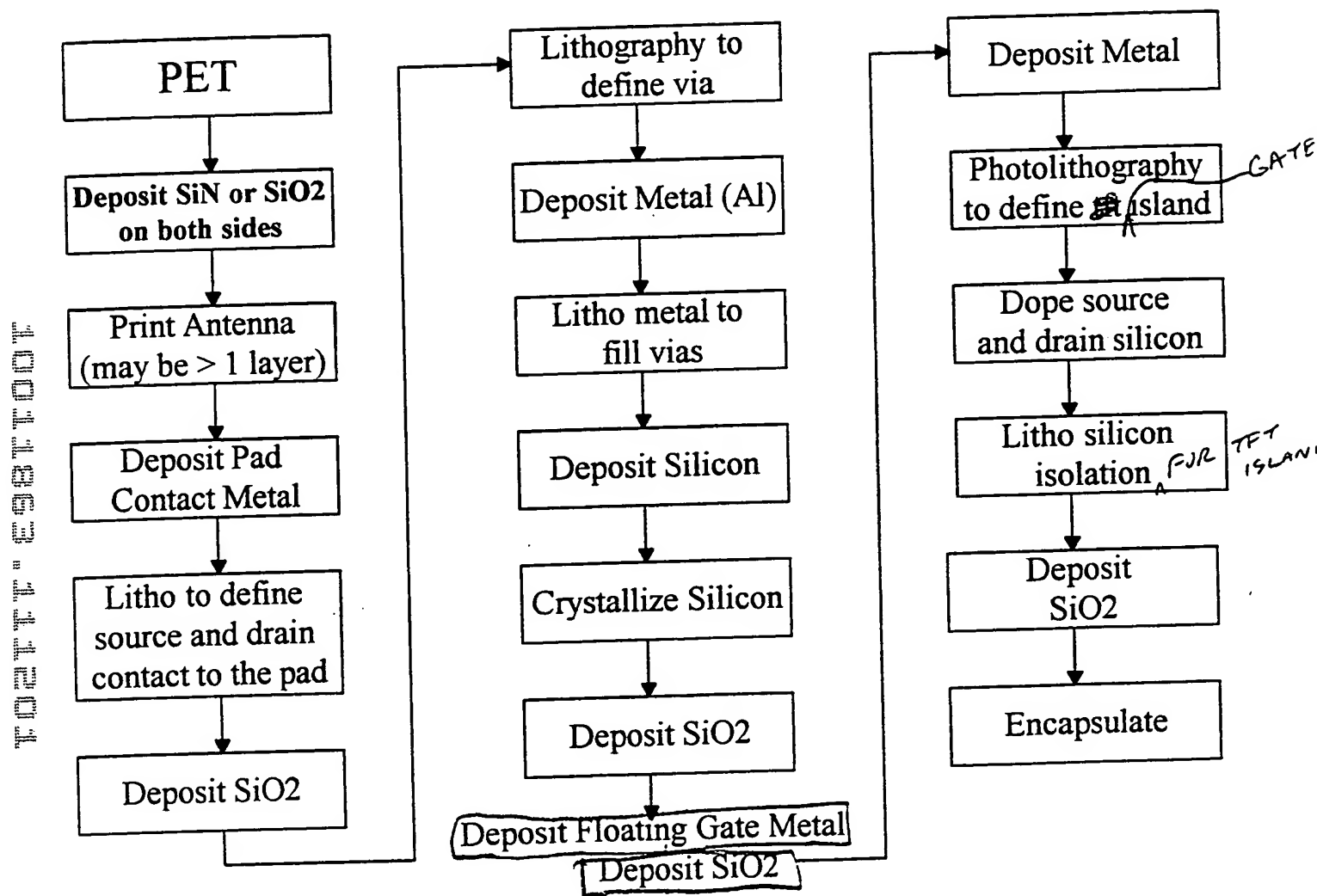
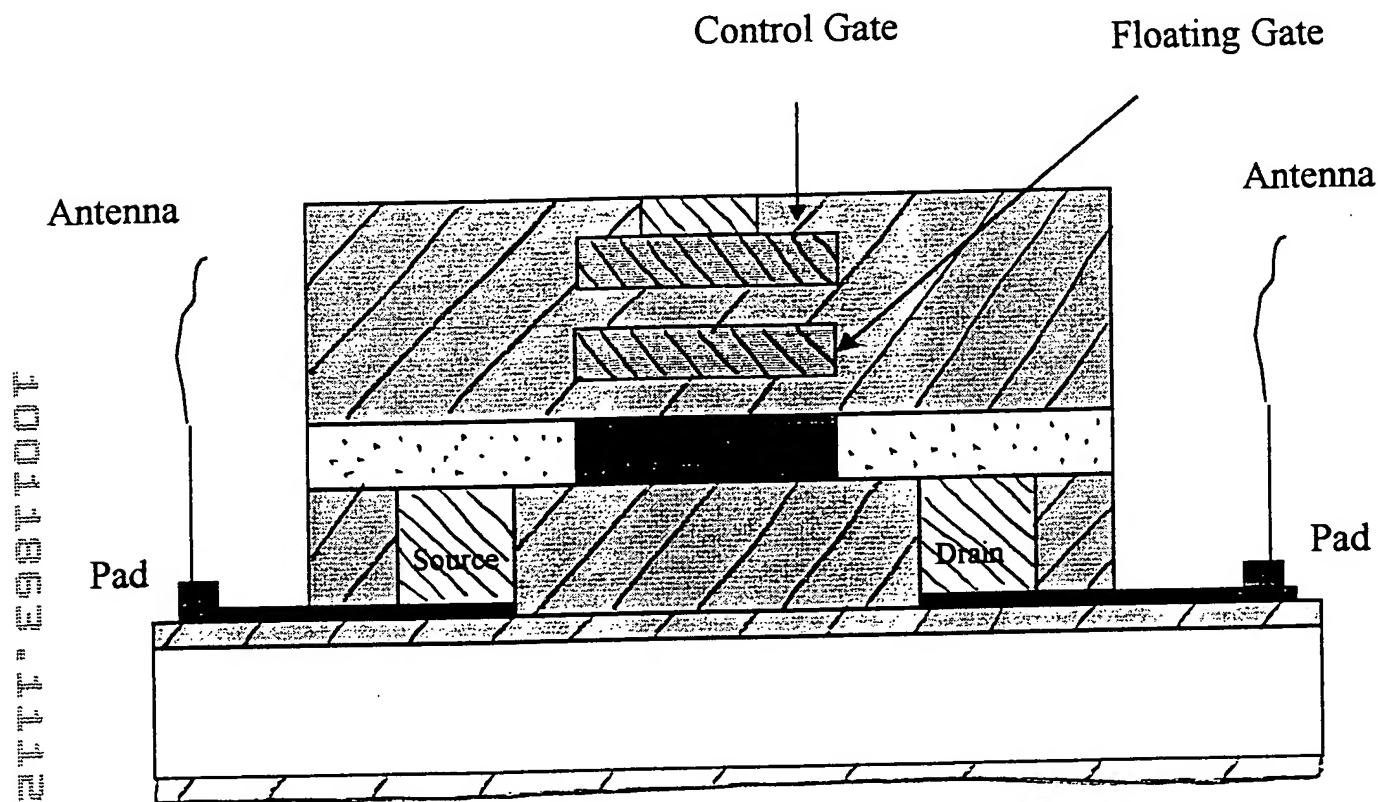


FIG. 9

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Do No.:  
Title:

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Schematic of a single EEPROM built on top of the printed antenna  
(in actual devices EEPROM and multiple transistors will be connected to the contact pads)

FIG. 10